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Hunan University, China

\*CORRESPONDENCE  
Xingquan Ji,  
ji\_xq@sdust.edu.cn

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# Dynamic reconfiguration of distribution network considering the uncertainty of distributed generation and loads

Qi Liu, Lihong Zhang, Xingquan Ji\* and Huizhe Shi

College of Electrical Engineering and Automation, Shandong University of Science and Technology, Qingdao, China

This study presents a new methodology to perform the distribution network dynamic reconfiguration (DNDR), taking into consideration the stochastic variations of loads and distributed generation (DG) of power. To solve the heavy computational burden that exists in traditional algorithms of the DNDR, this study first establishes the nodal sensitivity models to calculate the nodal variations caused by nodal power variations. Then, the DNDR is executed utilizing a co-evolutionary algorithm with the goal of loss minimization. The stochastic power flow calculations (PFCs) based on the nodal sensitivity are performed in the DNDR to handle the power fluctuations of the DGs and loads. Finally, the modified IEEE 33-bus test system and a practical distribution system are used for simulations. The simulation results validate the quickness and effectiveness of the proposed DNDR method.

## KEYWORDS

dynamic reconfiguration, distributed generations, uncertainty, sensitivity, co-evolution

## 1 Introduction

### 1.1 Literature review

The distribution network reconfiguration (DNR) can adjust the network topology and improve the performance of distribution networks by combining the open/closed states of line switches. The goals of the DNR include reducing the power losses (Lothi et al., 2020), improving system reliability (Kiammehr et al., 2019), enhancing system resilience (Sun et al., 2022), reducing voltage drops (Song et al., 2020), and managing energy (Gao et al., 2022a). The DNR plays an important role in the distribution automation of the system.

It is usually formulated as a complex non-linear optimization problem. The solution algorithms for the DNR include intelligent optimization algorithms, heuristic algorithms, and mathematical programming algorithms. Intelligent optimization algorithms, such as the genetic algorithm, honeybee mating optimization algorithm, particle swarm optimization algorithm, and harmony search algorithm, have been successfully applied in DNR (Roosta et al., 2019; Jafari et al., 2020; Lotfi and Ghani, 2021; Hiraoui et al., 2022). However, the tremendous amount of computation of intelligent optimization algorithms greatly limits their engineering applications. The heuristic algorithms can solve the DNR problem with high speed. In the study by Haridi and Das (2023), a two-stage heuristic algorithm based on the power flow analysis is used for DNR. Zhao et al. (2020) proposed the switch opening and exchange method for DNR. Recently, efficient mathematical programming algorithms, such as convex programming, have been

# Dynamic Reconfiguration

**RD Boyd**



## **Dynamic Reconfiguration:**

**Dynamic Reconfiguration** Ramachandran Vaidyanathan, Jerry Trahan, 2004-01-31 *Dynamic Reconfiguration Architectures and Algorithms* offers a comprehensive treatment of dynamically reconfigurable computer architectures and algorithms for them. The coverage is broad starting from fundamental algorithmic techniques ranging across algorithms for a wide array of problems and applications to simulations between models. The presentation employs a single reconfigurable model, the reconfigurable mesh, for most algorithms to enable the reader to distill key ideas without the cumbersome details of a myriad of models. In addition to algorithms, the book discusses topics that provide a better understanding of dynamic reconfiguration such as scalability and computational power and more recent advances such as optical models, run-time reconfiguration on FPGA and related platforms, and implementing dynamic reconfiguration. The book, featuring many examples and a large set of exercises, is an excellent textbook or reference for a graduate course. It is also a useful reference to researchers and system developers in the area. [Dynamic Reconfiguration in a Modular Computer System](#) Roger R. Schell, 1971

**Dynamic Reconfiguration in Real-Time Systems**, 2012-08-31

**Component Deployment** Judith Bishop, 2002-06-14. Deployment is the act of taking components and readying them for productive use. There may be steps following deployment such as installation or management-related functions, but all decisions about how to configure and compose, assemble a component are made at the deployment stage. This is therefore the one opportunity in the software lifecycle to bridge the gap between what the component developer couldn't know about the deployment environment and what the environment's developer couldn't know about the open set of deployable components. It is not surprising that deployment as a dedicated step gains importance when addressing issues of system-wide qualities such as coping with constrained resources or preparing for component adaptation and system evolution. Yet component deployment is still a discipline in its infancy; it became mainstream practice only in the mid-1990s. Much of the best practice impulse originated in products like Microsoft's Transaction Server and its approach to attribute-based programming and later products like Enterprise JavaBeans and now the Corba Component Model. All these address the specific needs of enterprise application servers. However, the potential of the deployment concept goes far beyond this. Deployment can and should touch effectively all truly component-based solutions. The proceedings of *Component Deployment 2002* represent a good cross-section of the gamut of deployment issues. From customization to address source constraints to reconfiguration of deployed systems and from architecture to design to languages, the avid reader will find some contribution.

**Risk, Reliability and Safety: Innovating Theory and Practice** Lesley Walls, Matthew Revie, Tim Bedford, 2016-11-25. *Risk, Reliability and Safety* contains papers describing innovations in theory and practice contributed to the scientific programme of the European Safety and Reliability conference ESREL 2016 held at the University of Strathclyde in Glasgow, Scotland, 25-29 September 2016. Authors include scientists, academics, practitioners, regulators, and other key individuals with expertise and experience relevant to specific areas. Papers include

domain specific applications as well as general modelling methods Papers cover evaluation of contemporary solutions exploration of future challenges and exposition of concepts methods and processes Topics include human factors occupational health and safety dynamic and systems reliability modelling maintenance optimisation uncertainty analysis resilience assessment risk and crisis management

**Dynamic Reconfiguration in a Modular Computer Systems** Roger R. Schell,1971

**Advanced Parallel Processing Technologies** Xingming Zhou,Stefan Jähnichen,Ming Xu, Jiannong Cao,2003-09-09 This volume contains the papers presented at the 5th International Workshop on Advanced Parallel Processing Technologies APPT 2003 This series of workshops is designed to strengthen the cooperation between the German and Chinese institutions active in the area of these technologies It has continued to grow providing an excellent forum for reporting advances in parallel processing technologies The 5th workshop itself addressed the entire gamut of related topics ranging from the architectural aspects of parallel computer hardware and system software to the applied technologies for novel applications For this workshop we received over 191 full submissions from researchers all over the world All the papers were peer reviewed in depth and qualitatively graded on their relevance originality significance presentation and the overall appropriateness for their acceptance Any concerns raised were discussed in the program committee The organizing committee did an excellent job in selecting 78 papers Among them 21 were short ones for presentation In short the papers included here represent the forefront of research from China Germany and the other countries

Dynamic Reconfiguration in Real-Time Systems Weixun Wang,Prabhat Mishra,Sanjay Ranka,2014-08-08 Given the widespread use of real time multitasking systems there are tremendous optimization opportunities if reconfigurable computing can be effectively incorporated while maintaining performance and other design constraints of typical applications The focus of this book is to describe the dynamic reconfiguration techniques that can be safely used in real time systems This book provides comprehensive approaches by considering synergistic effects of computation communication as well as storage together to significantly improve overall performance power energy and temperature

**A State Machine Approach to Reliable and Dynamically Reconfigurable Distributed Systems** Alvin Sek See Lim,1993

Dynamic System Reconfiguration in Heterogeneous Platforms Nikolaos Voros,Alberto Rosti,Michael Hübner,2009-05-28 Dynamic System Reconfiguration in Heterogeneous Platforms defines the MORPHEUS platform that can join the performance density advantage of reconfigurable technologies and the easy control capabilities of general purpose processors It consists of a System on Chip made of a scalable system infrastructure hosting heterogeneous reconfigurable accelerators providing dynamic reconfiguration capabilities and data stream management capabilities

**Evolvable Systems** ,1996

*A Method for Dynamic Reconfiguration of a Cognitive Radio System* ,2006 Advances in process technology has made it possible to migrate applications that were traditionally implemented in custom silicon to general purpose processors This transition has given birth to the field of cognitive and software defined radio C SDR These C SDRs offer a broad range of opportunities for

improving the use and utilization of radio frequency spectrum This includes the creation of radio networks that can reconfigure their operation based on application requirements policy updates environmental conditions and the ability to adapt to a wide range of protocols One of the key benefits of having a C SDR is its ability to change communication parameters in response to changes in application needs and or changes in the radio frequency landscape Such reconfiguration requires an understanding of how these communication parameters interact within the network protocol stack Analysis of these parametric cross layer interactions is a critical precursor in the development of a predictive model and algorithm for dynamic reconfiguration of a C SDR This work investigates how parameters at the physical data link network and application layers interact how desirable configurations of these parameters can be determined and how these parameters affect the performance of file transfer and Voice over IP applications An analysis of varying communication parameters across networking layers is used to inform the design implementation and evaluation of a predictive model and algorithm for dynamic reconfiguration of a cognitive radio This model and algorithm allow a C SDR to dynamically modify its configuration in order to improve system performance A systematic method for development of a cognitive platform is presented This method uses statistical analysis of variance and design of experiments techniques to inform the design and implementation of a dynamic reconfiguration algorithm

**1997 International Conference on Parallel and Distributed Systems** IEEE Computer Society. TC on Distributed Processing,1997 Aimed at researchers professors practitioners students and other computing professionals this work looks at architectures parallel and distributed computation networks mobile computing and communication parallel language and compiler and cache memory

22nd International Conference on Distributed Computing Systems Luis E. T. Rodrigues,Wen-Shyen Eric Chen,2002 This year s program covers areas such as distributed agents and intelligent networks internet and web computing network protocols distributed operating systems distributed databases middleware and distributed platforms mobile computing distributed algorithms fault tolerant systems distributed systems security

IEEE Symposium on FPGAs for Custom Computing Machines Kenneth L. Pocek,Jeffrey M. Arnold,1996 Contains papers from the April 1996 symposium reporting on research in device architectures applications compilers and related software support Topics include using MORRPH in an industrial machine vision system a software development system for FPGA based data acquisition systems a reconfigura

IEEE International Symposium on Industrial Electronics Proceedings ,2002

**Optical Engineering** ,2005 Publishes papers reporting on research and development in optical science and engineering and the practical applications of known optical science engineering and technology

**Design Patterns for Developing Dynamically Adaptive Systems** Andres J. Ramirez,2008 *Proceedings* ,1994

**GLOBECOM Tokyo '87** ,1987

## Enjoying the Beat of Phrase: An Emotional Symphony within **Dynamic Reconfiguration**

In a world taken by displays and the ceaseless chatter of immediate interaction, the melodic beauty and psychological symphony created by the written word often disappear in to the backdrop, eclipsed by the constant noise and interruptions that permeate our lives. But, located within the pages of **Dynamic Reconfiguration** an enchanting fictional value full of organic emotions, lies an immersive symphony waiting to be embraced. Crafted by a wonderful musician of language, this charming masterpiece conducts viewers on an emotional trip, well unraveling the hidden tunes and profound affect resonating within each carefully crafted phrase. Within the depths of this touching evaluation, we shall discover the book is key harmonies, analyze their enthralling publishing model, and submit ourselves to the profound resonance that echoes in the depths of readers souls.

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