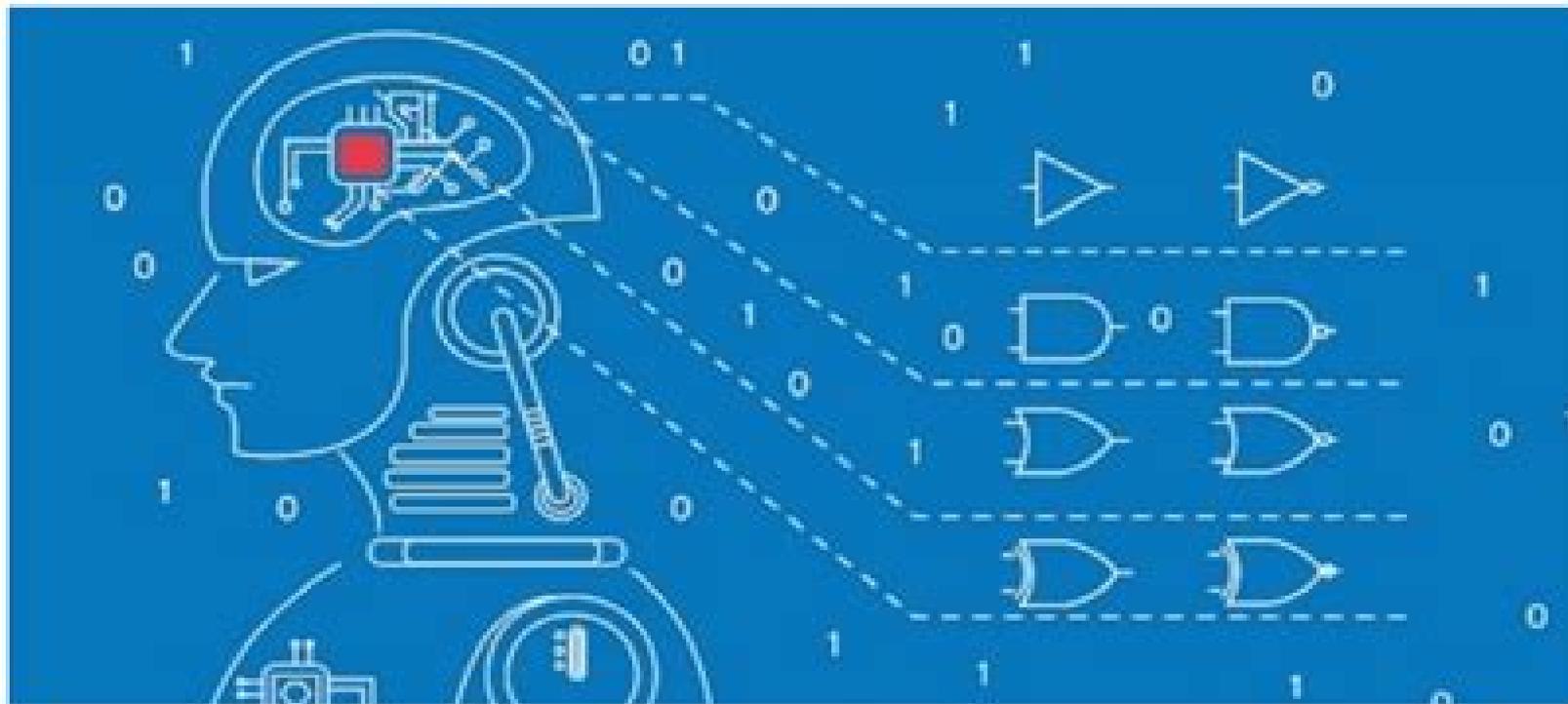


Inputs		Output
p	q	
1	1	0
1	0	1
0	1	0
0	0	0



Digital Logic Design And Applications An Experimental Approach

G Psacharopoulos



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