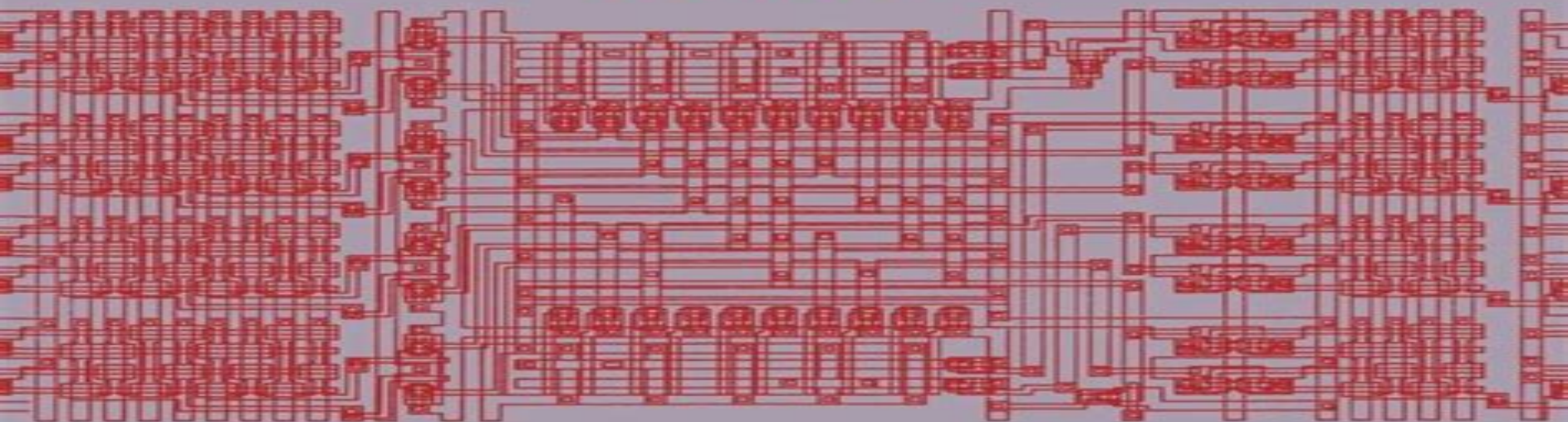


ADVANCED SIMULATION AND
TEST METHODOLOGIES FOR

VLSI

DESIGN



Gordon Russell and Ian L. Sayers

Van Nostrand Reinhold (International)

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