

CODE OPTIMIZATION: EFFECTIVE MEMORY USAGE

```
push bp
mov bp, sp
xor ax, ax
call @_StackCheck$@Word /
word_2030, 08C83h
word_2032, 08851h
word_2034, 551Eh
word_2036, 3083h
word_2038

SystemInit / word_2032
WINCRInit / word_2036
mov si, word_2038
mov di, word_203A
call @$_plus$@RealTI / RealTI
push bx
push ax
call
```

- Be informed! Understand typical mistakes made by programmers
- Get practical skills and undocumented solutions for creating faster programs
- Eliminate problems with effective programming techniques
- Overclock the memory to reach maximum performance

KRIS KASPERSKY



Code Optimization Effective Memory Usage

**Genadiy Pivnyak, Olexandr
Beshta, Mykhaylo Alekseyev**



Code Optimization Effective Memory Usage:

Code Optimization Kris Kaspersky,2003 A guide to optimizing programs on the PC and Unix platforms this book covers the expediency of optimization and the methods to increase the speed of programs via optimization Discussed are typical mistakes made by programmers that lessen the performance of the system along with easily implemented solutions Detailed descriptions of the devices and mechanism of interaction of the computer components effective ways of programming and a technique for optimizing programs are provided Programmers will also learn how to effectively implement programming methods in a high level language that is usually done in assembler with particular attention given to the RAM subsystem The working principles of the RAM and the way in which it is coupled with the processor as well as a description of programming methods that allows programmers to overclock the memory to reach maximum performance are included *Code Optimization: Effective Memory Usage* Kris Kaspersky,2003 This book covers methods that can be used to increase the speed of programs via optimisation Typical mistakes made by programmers that lessen the performance of the system are discussed along with easily implemented solutions Detailed descriptions of the devices and mechanisms of interaction between the computer components effective ways of programming and a technique for optimising programs are provided Programmers also will learn how to effectively implement in a high level language programming methods that are usually done in the assembler Particular attention is given to the RAM and cache subsystems The working principles of RAM and the way in which it is coupled with the processor as well as descriptions of programming methods that allow programmers to over clock the memory to reach maximum performance are included It is oriented toward application programmers with some experience in C C programming we well as toward system programmers with knowledge of the assembly language However the optimisation techniques described here are not bound to any high level programming language Thus knowledge of C is required only for understanding the source code provided in this book This book also will be useful to hardware specialists and technicians especially those who build and fine tune computers It provides detailed descriptions of the hardware operating principles and discusses the bottlenecks of the most common hardware components [Enhancing Java EE Applications: Strategies for Performance and Scalability](#) Peter Jones,2025-01-09 Unlock the full potential of your Java EE applications with *Enhancing Java EE Applications Strategies for Performance and Scalability* This essential guide provides comprehensive coverage of techniques and strategies to boost the performance and scalability of your Java EE applications From architectural insights to advanced coding practices each chapter brims with expert advice and practical solutions to help you overcome performance and scalability challenges Learn how to profile and monitor applications to pinpoint bottlenecks optimize Java EE servers effectively interact with databases and implement robust concurrency and multithreading Explore caching strategies fine tune application code and master network communications within a Java EE context Additionally discover how to manage memory efficiently and deploy resources wisely to ensure maximum stability

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Performance Engineering Best Practices Nolan V. Jones,2025-01-22 DESCRIPTION Elevate your application s performance with *Performance Engineering Best Practices* This comprehensive guide provides the knowledge and techniques to optimize your software s speed scalability and reliability Learn the skills of performance engineering refine your craft identify and eliminate performance bottlenecks and ensure your applications deliver exceptional user experiences This book explores *Performance Engineering* covering foundational concepts advanced techniques and emerging trends It defines performance engineering versus performance testing highlights its role in organizational success and stresses integrating performance throughout development Key topics include service level objectives SLOs cloud performance challenges and balancing capacity with costs The book details optimization techniques for cloud platforms Java databases and architectural styles while addressing observability with logging error handling and alert strategies It concludes with insights into AI ML integration and the impact of evolving technologies After reading the book the reader will gain a holistic understanding of

performance and what to do with the design process implementation and testing stages of the development lifecycle to ensure fast IT platforms

KEY FEATURES Learn to build high performance IT platforms enhance runtime environments and use Kubernetes effectively Discover methods to accelerate cloud platform delivery while ensuring quality with automated performance checks Understand efficient design patterns and coding practices to maintain fast running distributed platforms without extensive code refactoring

WHAT YOU WILL LEARN Understand the basics of performance engineering and how it differs from Performance Testing Learn to apply performance principles throughout the software development lifecycle SDLC Explore cloud specific challenges like scaling regional latency and managing costs Master techniques for optimizing JVM writing efficient Java code and database performance Discover tools for performance automation observability alerting and creating scalable architectures

WHO THIS BOOK IS FOR This book is for site reliability engineers architects developers managers and performance engineers It showcases a holistic understanding of processes related to performance engineering for building fast IT platforms

TABLE OF CONTENTS 1 Introduction to Performance Engineering 2 Building a Performance Practice 3 Challenges and Realities of Cloud Performance 4 Cloud Environment and Scalability 5 Performance Automation 6 Cloud Web Platform Optimization 7 Java Virtual Machine Optimization 8 Java Code Optimization 9 Database Optimization 10 High Performance Architectures 11 Error Handling and Logging 12 Cloud Observability and Cloud Profiling 13 Alerting Strategies 14 Future Trends in Performance Engineering

Power Engineering and Information Technologies in Technical Objects Control Genadiy Pivnyak, Olexandr Beshta, Mykhaylo Alekseyev, 2017-02-03 Improved knowledge in the field of technical objects operation and control helps manufacturers to decrease energy consumption and keep construction costs low Moreover it helps dealing effectively with environmental problems and switching to renewable forms of energy on the path of sustainable development of the society The methods and technologies presented in this book will allow to improve the effectiveness of technical objects control and helps achieving safe economical high quality usage of power engineering and information technologies The book presents recent advances in power engineering electric drives transport systems power electronics cybersecurity and others Vital issues of innovative small vehicles with using hydrogen fuel as well as boring rigs and underwater hydraulic transport pipelines are considered The book offers a fresh look at energy saving and energy efficiency in industry new ideas in information technologies paying much attention to interdisciplinary specification of the results obtained

Javascript Mastery: In-Depth Techniques and Strategies for Advanced Development Adam Jones, 2024-12-13 Unleash your Javascript expertise with Javascript Mastery In Depth Techniques and Strategies for Advanced Development the ultimate resource for developers committed to refining their programming prowess This comprehensive guide takes you on a detailed exploration of sophisticated concepts ensuring a deep understanding of the Javascript engine runtime and the finesse of both functional and object oriented programming Excel in asynchronous programming for seamless operational flow harness the power of the DOM and browser events to craft interactive user

interfaces and utilize advanced data structures for optimal data management Engaging with modern Javascript including ES6 and beyond this book equips you to compose cleaner more efficient code by embracing the latest features and standards Delve into performance optimization memory management and learn the pragmatic approaches to building and deploying robust applications using Node js and npm Each chapter is thoughtfully crafted to build upon the last offering a well rounded and cohesive learning experience Whether you aim to sharpen your skills in frontend or backend development Javascript Mastery In Depth Techniques and Strategies for Advanced Development provides the insights and practical tools required to conquer complex programming tasks Elevate your Javascript mastery and effortlessly develop high quality transformative applications

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Electronics, Communications and Networks IV Amir Hussain,Mirjana Ivanovic,2015-07-01 The 4th International Conference on Electronic Communications and Networks CECNet2014 inherits the fruitfulness of the past three conferences and lays a foundation for the forthcoming next year in Shanghai CECNet2014 was hosted by Hubei University of Science and Technology China with the main objective of providing a comprehensive global forum for experts and participants from acadamia to exchange ideas and presenting results of ongoing research in the most state of the art areas of Consumer Electronics Technology Communication Engineering and Technology Wireless Communications Enginneering and Technology and Computer Engineering and Technology In this event 13 famous scholars and Engineers have delivered the

keynote speeches on their latest research including Prof Vijaykrishnan Narayanan a Fellow of the Institute of Electrical and Electronics Engineers Prof Han Chieh Chao the Director of the Computer Center for Ministry of Education Taiwan from September 2008 to July 2010 Prof Borko Furht the founder of the Journal of Multimedia Tools and Applications Prof Kevin Deng who served as Acting Director of Hong Kong APAS R D Center in 2010 and Prof Minh Jo the Professor of Department of Computer and Information Science Korea University

Digital Recycling: The Architecture of Object Pooling Aaron Crossman, 2006-03-26 When a player fires a machine gun in a modern video game the engine does not create a new bullet. Generating and deleting thousands of unique digital objects per second would instantly overwhelm the console's random access memory causing a catastrophic crash. Instead developers rely on a brilliant algorithmic cheat: object pooling. This invisible programming architecture creates a finite pool of invisible bullets. When the game loads, when a weapon is fired, the game simply teleports an existing hidden bullet to the barrel, activates it, and then teleports it back to the invisible vault once it hits a wall. This technical deep dive exposes the extreme memory constraints of game development. It explains how almost everything in a virtual world, from falling rain to exploding shrapnel, is actually a recycled asset being shuffled behind the curtain at lightning speed. Understand the microscopic math keeping your favorite digital worlds from collapsing. Discover how the relentless pursuit of hardware efficiency forces programmers to become masters of digital sleight of hand. *The Publishers Weekly*, 2003

Memory Model Based Algorithmic Computational Optimizations for Signal Processing Gene Cheung, 2000 *The Indian National Bibliography* B. S. Kesavan, 2004-11

Proceedings of the ... ACM Great Lakes Symposium on VLSI, 2006 **ACM SIGPLAN Notices**, 2006 *IBM Journal of Research and Development*, 2006 Conference Proceedings of the 1997 International Conference on Supercomputing, 1997 *Proceedings Parallel Computing Journal of Zhejiang University*, 2007 *Proceedings*, 2005

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