

What are Asynchronous Circuits?

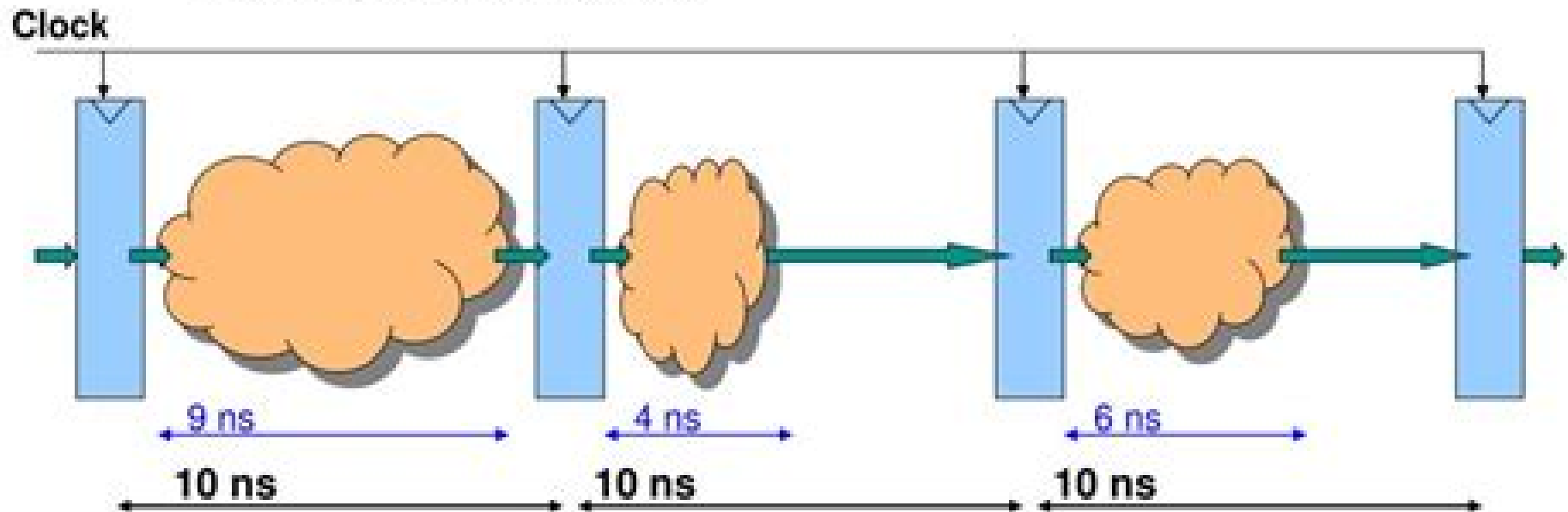
■ Synchronous Circuits

– Everything synchronized to a global clock

- Clock edges determine the time instants where data is sampled
- Register inputs are sampled at the clock rising edge
- Data wires may glitch between clock edges

– “Worst case” operation:

- The clock frequency is limited by the speed of the slowest stage.
- The clock frequency must be slow enough that the circuit will work with worst case PVT, and worst case data.



Asynchronous Circuits Systems 2003

R Barnett



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