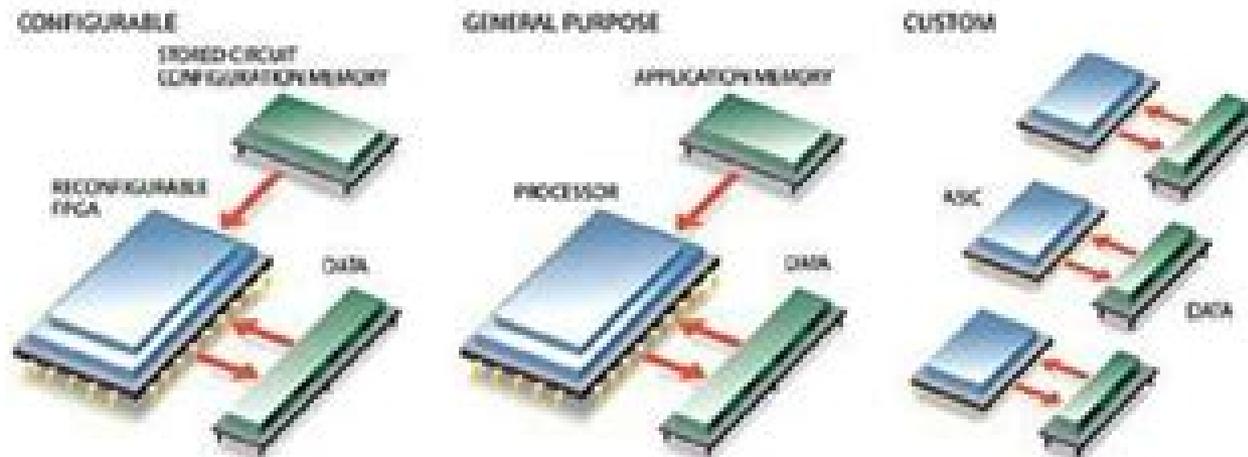


Configurable Computing Architectures

- Configurable Computing architectures combine elements of general-purpose computing and application-specific integrated circuits (ASICs).
 - The general-purpose processor operates with fixed circuits that perform multiple tasks under the control of software.
 - An ASIC contains circuits specialized to a particular task and thus needs little or no software to instruct it.
- The configurable computer can execute software commands that alter its configurable devices (e.g. FPGA circuits) as needed to perform a variety of jobs.



Configurable Computing

Christophe Bobda



Configurable Computing:

Configurable Computing, 1998 This collection of papers presented at the IS T SPIE Electronic Imaging Symposium includes articles on a variety of relevant issues and topics Field-Programmable Logic and Applications: Reconfigurable Computing Is Going Mainstream Manfred Glesner, Peter Zipf, Michel Renovell, 2003-08-02 This book constitutes the refereed proceedings of the 12th International Conference on Field Programmable Logic and Applications FPL 2002 held in Montpellier France in September 2002 The 104 revised regular papers and 27 poster papers presented together with three invited contributions were carefully reviewed and selected from 214 submissions The papers are organized in topical sections on rapid prototyping FPGA synthesis custom computing engines DSP applications reconfigurable fabrics dynamic reconfiguration routing and placement power estimation synthesis issues communication applications new technologies reconfigurable architectures multimedia applications FPGA based arithmetic reconfigurable processors testing and fault tolerance crypto applications multitasking compilation techniques etc The Computer Engineering Handbook Vojin G. Oklobdzija, 2001-12-26 There is arguably no field in greater need of a comprehensive handbook than computer engineering The unparalleled rate of technological advancement the explosion of computer applications and the now in progress migration to a wireless world have made it difficult for engineers to keep up with all the developments in specialties outside their own References published only a few years ago are now sorely out of date The Computer Engineering Handbook changes all of that Under the leadership of Vojin Oklobdzija and a stellar editorial board some of the industry's foremost experts have joined forces to create what promises to be the definitive resource for computer design and engineering Instead of focusing on basic introductory material it forms a comprehensive state of the art review of the field's most recent achievements outstanding issues and future directions The world of computer engineering is vast and evolving so rapidly that what is cutting edge today may be obsolete in a few months While exploring the new developments trends and future directions of the field The Computer Engineering Handbook captures what is fundamental and of lasting value Embedded Computer Systems: Architectures, Modeling, and Simulation Timo D. Härmäläinen, Andy D. Pimentel, Jarmo Takala, Stamatis Vassiliadis, 2005-07-11 The SAMOS workshop is an international gathering of highly qualified researchers from academia and industry sharing in a 3 day lively discussion on the quiet and spiring northern mountainside of the Mediterranean island of Samos As a tradition the workshop features workshop presentations in the morning while after lunch all kinds of informal discussions and nut cracking gatherings take place The workshop is unique in the sense that not only solved research problems are presented and discussed but also partly unsolved problems and in depth topical reviews can be unleashed in the scientific arena Consequently the workshop provides the participants with an environment where collaboration rather than competition is fostered The earlier workshops SAMOS I IV 2001 2004 were composed only of invited presentations Due to increasing expressions of interest in the workshop the Program Committee of SAMOS V decided to open the workshop for all

submissions As a result the SAMOS workshop gained an immediate popularity a total of 114 submitted papers were received for evaluation The papers came from 24 countries and regions Austria 1 Belgium 2 Brazil 5 Canada 4 China 12 Cyprus 2 Czech Republic 1 Finland 15 France 6 Germany 8 Greece 5 Hong Kong 2 India 2 Iran 1 Korea 24 The Netherlands 7 Pakistan 1 Poland 2 Spain 2 Sweden 2 Taiwan 1 Turkey 2 UK 2 and USA 5 We are grateful to all of the authors who submitted papers to the workshop

Advances in Recent Trends in Communication and Networks, 2010 *Digital Systems and Applications* Vojin G. Oklobdzija, 2017-12-19 New design architectures in computer systems have surpassed industry expectations Limits which were once thought of as fundamental have now been broken Digital Systems and Applications details these innovations in systems design as well as cutting edge applications that are emerging to take advantage of the fields increasingly sophisticated capabilities This book features new chapters on parallelizing iterative heuristics stream and wireless processors and lightweight embedded systems This fundamental text Provides a clear focus on computer systems architecture and applications Takes a top level view of system organization before moving on to architectural and organizational concepts such as superscalar and vector processor VLIW architecture as well as new trends in multithreading and multiprocessing includes an entire section dedicated to embedded systems and their applications Discusses topics such as digital signal processing applications circuit implementation aspects parallel I/O algorithms and operating systems Concludes with a look at new and future directions in computing Features articles that describe diverse aspects of computer usage and potentials for use Details implementation and performance enhancing techniques such as branch prediction register renaming and virtual memory Includes a section on new directions in computing and their penetration into many new fields and aspects of our daily lives

High-speed Computing, Digital Signal Processing, and Filtering Using Reconfigurable Logic John Schewel, Society of Photo-optical Instrumentation Engineers, 1996

IEEE Symposium on FPGAs for Custom Computing Machines Kenneth L. Pocek, Jeffrey M. Arnold, 1996 Contains papers from the April 1996 symposium reporting on research in device architectures applications compilers and related software support Topics include using MORRPH in an industrial machine vision system a software development system for FPGA based data acquisition systems a reconfigura

Introduction to Reconfigurable Computing Christophe Bobda, 2007-09-30 Introduction in Reconfigurable Computing provides a comprehensive study of the field Reconfigurable Computing It provides an entry point to the novice willing to move in the research field reconfigurable computing FPGA and system on programmable chip design The book can also be used as teaching reference for a graduate course in computer engineering or as reference to advance electrical and computer engineers It provides a very strong theoretical and practical background to the field of reconfigurable computing from the early Estrin's machine to the very modern architecture like coarse grained reconfigurable device and the embedded logic devices Apart from the introduction and the conclusion the main chapter of the book are the following Architecture of reconfigurable systems which presents the technology and the architecture used in fine grained

and those used in coarse grained reconfigurable devices Design and implementation This section deals with the implementation on reconfigurable system It briefly covers the steps needed to implement application on today s FPGAs And focus on the logic synthesis for FPGA in particular LUT technology mapping High Level Synthesis for Reconfigurable Devices The high level synthesis for reconfigurable systems also known as temporal partitioning is presented here Several temporal partitioning techniques are presented and explained Temporal placement This section considers stand alone reconfigurable systems Its assume that a kind of operating systems for reconfigurable systems is in charge of managing the resources of a given system and allocate space on a device for the computation of incoming tasks and therefore presents several temporal placement approaches for off line as well as on line placement On line and Dynamic Interconnection This chapter reviews andexplains the different approaches for allowing communication between modules dynamically placed at run time on a given device Designing a reconfigurable application on Xilinx Virtex FPGA In this section the different design approaches of partial reconfigurable systems on the Xilinx FPGAs that are one of the few one on the market with this feature is explained System on programmable chip System on programmable chip is a hot topic in reconfigurable computing This is mainly the integration of a system made upon some peripheral UART Ethernet VGA etc but also computational Coding filter etc hardware modules on one programmable chip The current usable solutions are presented The book furthermore focusses on the development of adaptive multiprocessors on chip i e systems consisting of a set of Processors and exchangeable hardware accelerators Applications This part covers the use of reconfigurable system in computer architecture rapid prototyping reconfigurable supercomputer reconfigurable massively parallel computers and algorithm better adapted for reconfigurable systems distributed arithmetic network packet processing etc

Field Programmable Gate Arrays (FPGAs) for Fast Board Development and Reconfigurable Computing John Schewel, Society of Photo-optical Instrumentation Engineers, 1995 **Proceedings** ,2005 *The Design and Implementation of a 200k-gate Reconfigurable Computing System* Feng Yun, 1997 *Reconfigurable Technology* ,1999 *Reconfigurable Technology* John Schewel, 1999 A collection of 19 papers on logical and practical aspects of field programmable gate arrays FPGAs for computing and applications *Field-programmable Logic and Applications* ,2002 *Hardware-assisted Fast Routing for Runtime Reconfigurable Computing* Randy Ren-Fu Huang, 2004 [Reconfigurable Systems](#) Institution of Electrical Engineers, 1999 **Parallel and Distributed Processing** ,2000 **Proceedings of Technical Papers** ,1999 [Programmable Logic Handbook](#) Ashok K. Sharma, 1998 This practice oriented guide to programming with Field Programmable Logic Devices is the most complete resource on the subject FPLDs are an essential part of today s high performance electronic systems because they save board space use less power and offer quicker turnaround times than traditional integrated circuits However to maximize FPLDs designers must understand and get around the tradeoffs involved This one stop guide addresses the challenges and opportunities through detailed coverage of FPGAs PLDs PLAs and CPLDs the high level description languages

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