



Demystifying Chipmaking

Richard F. Nade - Michael Hynes - Anne K. Miller

Demystifying Technology Series

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Demystifying Chipmaking

Richard Bailey



Demystifying Chipmaking:

Demystifying Chipmaking Richard F. Yanda, Michael Heynes, Anne Miller, 2005-06-06 This book takes the reader through the actual manufacturing process of making a typical chip from start to finish including a detailed discussion of each step in plain language The evolution of today's technology is added to the story as seen through the eyes of the engineers who solved some of the problems The authors are well suited to that discussion since they are three of those same engineers They have a broad exposure to the industry and its technology that extends all the way back to Shockley Laboratories the first semiconductor manufacturer in Silicon Valley The CMOS Complementary Metal Oxide Semiconductor process flow is the focus of the discussion and is covered in ten chapters The vast majority of chips made today are fabricated using this general method In order to ensure that all readers are comfortable with the vocabulary the first chapter carefully and clearly introduces the science concepts found in later chapters A chapter is devoted to pointing out the differences in other manufacturing methods such as the gallium arsenide technology that produces chips for cell phones In addition a chapter describing the nature of the semiconductor industry from a business perspective is included The entire process of making a chip is surprisingly easy to understand The part of the story that defies belief is the tiny dimensions the conducting wires and other structures on a chip are more than a hundred times thinner than a hair and getting thinner with every new chip design Authors are actual engineers who have a broad range of exposure and experience with chip technology Contains a unique chapter describing the nature of the semiconductor industry from a business perspective

Demystifying Chipmaking Elektronische Ressource Richard F. Yanda, 2005 **RF CMOS Oscillators for Modern Wireless**

Applications Masoud Babaie, Mina Shahmohammadi, Robert Bogdan Staszewski, 2022-09-01 While mobile phones enjoy the largest production volume ever of any consumer electronics products the demands they place on radio frequency RF transceivers are particularly aggressive especially on integration with digital processors low area low power consumption while being robust against process voltage temperature variations Since mobile terminals inherently operate on batteries their power budget is severely constrained To keep up with the ever increasing data rate an ever decreasing power per bit is required to maintain the battery lifetime The RF oscillator is the second most power hungry block of a wireless radio after power amplifiers Consequently any power reduction in an RF oscillator will greatly benefit the overall power efficiency of the cellular transceiver Moreover the RF oscillators purity limits the transceiver performance The oscillator's phase noise results in power leakage into adjacent channels in a transmit mode and reciprocal mixing in a receive mode On the other hand the multi standard and multi band transceivers that are now trending demand wide tuning range oscillators However broadening the oscillator's tuning range is usually at the expense of die area cost or phase noise The main goal of this book is to bring forth the exciting and innovative RF oscillator structures that demonstrate better phase noise performance lower cost and higher power efficiency than currently achievable Technical topics discussed in RF CMOS Oscillators for Modern Wireless

Applications include Design and analysis of low phase noise class F oscillators Analyze a technique to reduce 1 f noise up conversion in the oscillators Design and analysis of low power low voltage oscillators Wide tuning range oscillators Reliability study of RF oscillators in nanoscale CMOS

Graphene Optoelectronics Abdul Rashid bin M. Yusoff, 2014-08-25 This first book on emerging applications for this innovative material gives an up to date account of the many opportunities graphene offers high end optoelectronics The text focuses on potential as well as already realized applications discussing metallic and passive components such as transparent conductors and smart windows as well as high frequency devices spintronics photonics and terahertz devices Also included are sections on the fundamental properties synthesis and characterization of graphene With its unique coverage this book will be welcomed by materials scientists solid state chemists and solid state physicists alike

Sputtering Materials for VLSI and Thin Film Devices Jaydeep Sarkar, 2010-12-13 An important resource for students engineers and researchers working in the area of thin film deposition using physical vapor deposition e g sputtering for semiconductor liquid crystal displays high density recording media and photovoltaic device e g thin film solar cell manufacturing This book also reviews microelectronics industry topics such as history of inventions and technology trends recent developments in sputtering technologies manufacturing steps that require sputtering of thin films the properties of thin films and the role of sputtering target performance on overall productivity of various processes Two unique chapters of this book deal with productivity and troubleshooting issues The content of the book has been divided into two sections a the first section Chapter 1 to Chapter 3 has been prepared for the readers from a range of disciplines e g electrical chemical chemistry physics trying to get an insight into use of sputtered films in various devices e g semiconductor display photovoltaic data storage basic of sputtering and performance of sputtering target in relation to productivity and b the second section Chapter 4 to Chapter 8 has been prepared for readers who already have background knowledge of sputter deposition of thin films materials science principles and interested in the details of sputtering target manufacturing methods sputtering behavior and thin film properties specific to semiconductor liquid crystal display photovoltaic and magnetic data storage applications In Chapters 5 to 8 a general structure has been used i e a description of the applications of sputtered thin films sputtering target manufacturing methods including flow charts sputtering behavior of targets e g current voltage relationship deposition rate and thin film properties e g microstructure stresses electrical properties in film particles While discussing these topics attempts have been made to include examples from the actual commercial processes to highlight the increased complexity of the commercial processes with the growth of advanced technologies In addition to personnel working in industry setting university researchers with advanced knowledge of sputtering would also find discussion of such topics e g attributes of target design chamber design target microstructure sputter surface characteristics various troubleshooting issues useful Unique coverage of sputtering target manufacturing methods in the light of semiconductor displays data storage and photovoltaic industry requirements Practical information on technology trends role of sputtering

and major OEMs Discussion on properties of a wide variety of thin films which include silicides conductors diffusion barriers transparent conducting oxides magnetic films etc Practical case studies on target performance and troubleshooting Essential technological information for students engineers and scientists working in the semiconductor display data storage and photovoltaic industry

The British National Bibliography Arthur James Wells,2005 **American Book Publishing Record** ,2005 Electronics ,1986 *Student Lawyer* ,1988 **The New York Times Index** ,1994 **Making Microchips** Jan Mazurek,1998-12-07 An examination of the environmental and economic implications of the computer microchip industry s exodus from California s Silicon Valley to New Mexico Virginia Ireland and Taiwan In Making Microchips Jan Mazurek examines the environmental and economic implications of the computer microchip industry s exodus from California s Silicon Valley to New Mexico Virginia Ireland and Taiwan Globalization economic restructuring and changing manufacturing processes in this rapidly growing industry present difficult new questions for environmental policy Mazurek challenges the assumptions of U S policies designed to promote the competitiveness of domestic microchip makers She argues that although these initiatives focus on the economic effects of environmental regulation they fail to acknowledge how economic and organizational changes within the industry collide with and often confound efforts to monitor and manage pollution from chemicals used in microchip manufacturing Despite its reputation as a clean industry microchip manufacturing is fraught with hazards More than sixty dangerous acids solvents caustics and gases are used to make microchips and some of them are suspected to be carcinogens and or reproductive toxins Mazurek describes the environmental by products of chipmaking including soil contamination air and water pollution and damage to human health Applying insights from economic geography to questions of how and where companies organize production she shows how Silicon Valley played a pivotal role in the development of the microchip Pairing federal environmental data with structural and geographic information on the six firms that continue to build wafer fabrication plants in the United States she demonstrates how reorganization and relocation of manufacturing facilities divert attention from trends in toxic emissions and how they complicate public and private efforts to improve the industry s environmental performance In the concluding chapter Mazurek marshals her findings in a broader analysis of the expansion of global manufacturing and the resultant environmental problems

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