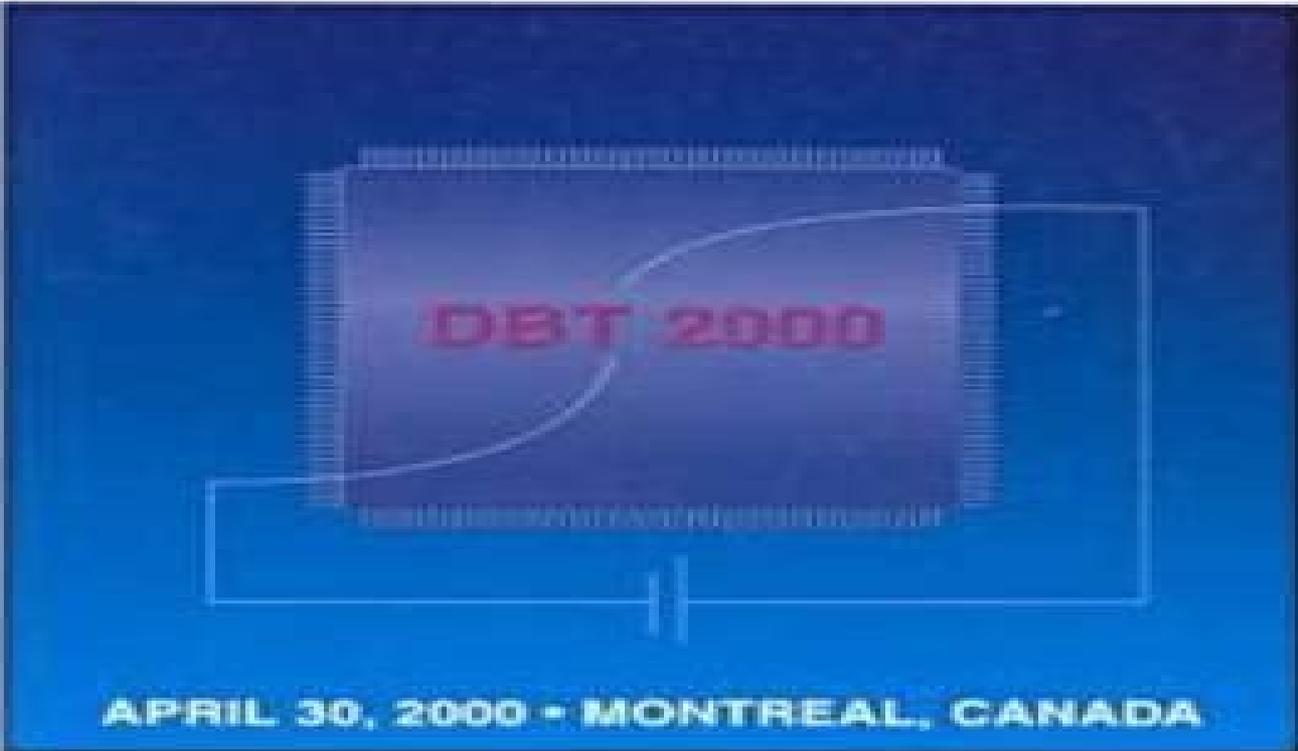


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Chakrabarty, 2017-12-19 Advances in design methods and process technologies have resulted in a continuous increase in the complexity of integrated circuits ICs. However, the increased complexity and nanometer size features of modern ICs make them susceptible to manufacturing defects as well as performance and quality issues. Testing for Small Delay Defects in Nanoscale CMOS Integrated Circuits covers common problems in areas such as process variations, power supply noise, crosstalk, resistive opens, bridges, and design for manufacturing (DfM) related rule violations. The book also addresses testing for small delay defects (SDDs) which can cause immediate timing failures on both critical and non-critical paths in the circuit. Overviews semiconductor industry test challenges and the need for SDD testing including basic concepts and introductory material. Describes algorithmic solutions incorporated in commercial tools from Mentor Graphics. Reviews SDD testing based on alternative methods that explore new metrics, top-off ATPG, and circuit topology based solutions. Highlights the advantages and disadvantages of a diverse set of metrics and identifies scope for improvement. Written from the triple viewpoint of university researchers, EDA tool developers, and chip designers and tool users, this book is the first of its kind to address all aspects of SDD testing from such a diverse perspective. The book is designed as a one-stop reference for current industrial practices, research challenges in the domain of SDD testing, and recent developments in SDD solutions. **2000**

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2005 IEEE International Workshop on Current and Defect Based Testing, 2005 Proceedings, 1995

Prognostics and Health Management of Electronics Michael G. Pecht, Myeongsu Kang, 2018-10-01 An indispensable guide for engineers and data scientists in design, testing, operation, manufacturing, and maintenance. A road map to the current challenges and available opportunities for the research and development of Prognostics and Health Management (PHM). This important work covers all areas of electronics and explains how to assess methods for damage estimation of components and systems due to field loading conditions, assess the cost and benefits of prognostic implementations, develop novel methods for in situ monitoring of products and systems in actual life cycle conditions, enable condition based predictive maintenance, increase system availability through an extension of maintenance cycles and/or timely repair actions, obtain knowledge of load history for future design qualification and root cause analysis, reduce the occurrence of no-fault-found (NFF), subtract life cycle costs of equipment from reduction in inspection costs, downtime, and inventory. Prognostics and Health

Management of Electronics also explains how to understand statistical techniques and machine learning methods used for diagnostics and prognostics Using this valuable resource electrical engineers data scientists and design engineers will be able to fully grasp the synergy between IoT machine learning and risk assessment

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Advanced Test Methods for SRAMs Alberto Bosio,Luigi Dilillo,Patrick Girard,Serge Pravossoudovitch,Arnaud Virazel,2009-10-08 Modern electronics depend on nanoscaled technologies that present new challenges in terms of testing and diagnostics Memories are particularly prone to defects since they exploit the technology limits to get the highest density This book is an invaluable guide to the testing and diagnostics of the latest generation of SRAM one of the most widely applied types of memory Classical methods for testing memory are designed to handle the so called static faults but these test solutions are not sufficient for faults that are emerging in the latest Very Deep Sub Micron VDSM technologies These new fault models referred to as dynamic faults are not covered by classical test solutions and require the dedicated test sequences presented in this book

Proceedings ,1929 **Proceedings of the ... International Symposium on the Physical & Failure Analysis of Integrated Circuits** ,2003 **Proceedings** Pennsylvania State University. Schoolmen's Week,1929 *Proceedings and Addresses* American Association on Mental Deficiency,1930 **Proceedings of the Annual Meeting - American Society for Testing Materials** American Society for Testing Materials,1925 Proceedings of SPIE--the International Society for Optical Engineering ,1999 *The Log* ,1933 *Proceedings of the 1988 IEEE International Conference on Microelectronic Test Structures, ICMTS* ,1988 *Proceedings of National Electric Light Association* National Electric Light Association. Convention,1927 **Proceedings** National Electric Light Association,1924 **IEEE VLSI Test Symposium** ,2005

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